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December 29, 2003

To: Commissioner for Patents
Mail Stop Application Numver
P.O. Box 1450
Alexandria, VA 22313-1450

From: Stephen B. Ackerman

Subject: Serial No.: 10/676,896
Filing Date: 10/01/03
Inventor: V. Ho, et al
Title: Process to Manufacture Nonvolatile MOS Memory Device

REQUEST TO CORRECT FILING RECEIPT

Dear Sir:

On the copy of the enclosed Filing Receipt, there is an error in the Title. The word **"Memory"** is misspelled. The corrected Title reads: **Process to manufacture nonvolatile MOS memory device.**

Please make the necessary change to correct the error on the Filing Receipt.

If there are any questions, please contact the undersigned attorney at (845) 452-5863. Thank you for your attention to this matter.

Respectfully Submitted,

Stephen B. Ackerman, Reg. No. 37,761

DEC. 27 2003



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/676,896	10/01/2003	2812	968	CS01-111	2	31	2

CONFIRMATION NO. 8079

FILING RECEIPT



OC000000011568737

Date Mailed: 12/24/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Chartered Semiconductor Manufacturing Ltd.;

Domestic Priority data as claimed by applicant

Foreign Applications

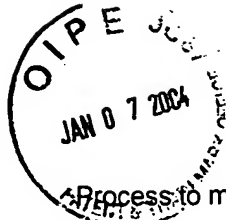
If Required, Foreign Filing License Granted: 12/24/2003

Projected Publication Date: 04/07/2005

Non-Publication Request: No

Early Publication Request: No

Title



Process to manufacture nonvolatile MOS memory device

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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